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Applicant: Tadao Ishibashi et al.

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AVALANCHE PHOTODIODE

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Art Unit: unknown

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INFORMATION DISCLOSURE CITATIONS MADE BY APPLICANT

U.S. Patent Documents

Examiner	Document	Issue		
<u>Initial*</u>	Number	<u>Date</u>	<u>Nan</u>	<u>ne</u>
1	6,104,047	08/15/2000	Watanabe	
		Foreign Patent Documents		
Examiner	Document	Publication	Country or	
<u>Initial</u> *	Number	Date	Patent Office	<u>Translation</u>
2	2000-022197	01/21/2000	Japan	No
3	61-198688	09/03/1986	Japan	No
4	2004-031707	01/29/2004	Japan	No
		Other Documents (including author, title, pertinent pages, etc.)		
		(morading datator, thie, per mem pages, etc.)		
Examiner				

Y. Muramoto et al., InP/InGaAs Pin Photodiode Structure Maximising Bandwidth and Efficiency, Electronics Letters, Vol. 39, No. 24, November 27, 2003, pp. 1749-1750.

Examiner: /Tifney Skyles/ (04/08/2009) Date Considered:

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.